

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Applicant:	§	Art Unit:	2185
Edward R. Rhoads et al.	§		
	§	Examiner:	Zhuo H. Li
Serial No.: 10/764,617	§		
	§	Conf. No.:	8924
Filed: January 26, 2004	§		
	§	Docket:	ITL.0241D1US
For: Organizing Information Stored in	§		P7376D
Non-Volatile Re-Programmable	§		
Semiconductor Memories	§	Assignee:	Intel Corporation

Mail Stop **RCE**  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**DECLARATION UNDER 37 C.F.R. § 1.132 (ATTRIBUTION)**

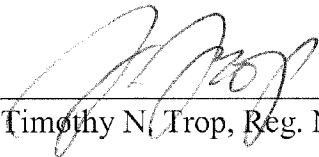
Sir:

The undersigned hereby states as follows:

1. I am the attorney who prosecuted both the pending application and the cited reference to Tallam.
2. A common disclosure was used as a matter of convenience for the two patent applications.
3. I obtained the material about putting the address for one section in another section including what is described in Figure 5 from inventors Rhoads and Ketrenos and put this material in the Tallam application. Thus, I was the person who put this information in the Tallam application. I obtained the information from Rhoads and Ketrenos.

4. I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and, further, that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Date: January 20, 2009

  
\_\_\_\_\_  
Timothy N. Trop, Reg. No. 28,994